EtherCAT
The real-time Ethernet fieldbus
EtherCAT
Ethernet for Control Automation Technology

Product overview
System overview

EtherCAT technology

XFC technology

EtherCAT Development
Products

EtherCAT components
Industrial PC
Embedded PC
EtherCAT Terminal
EtherCAT Box
EtherCAT Plug-in Modules
Infrastructure Components
EtherCAT Servo Drives
TwinCAT
TwinSAFE
# Product overview EtherCAT components

<table>
<thead>
<tr>
<th>EtherCAT components</th>
<th>EtherCAT Terminal</th>
<th>EtherCAT Box</th>
<th>EtherCAT Plug-in Modules</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>PC-based Control</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Industrial PCs</td>
<td>CPxxxx</td>
<td>EP1xxx</td>
<td>EJ1100</td>
</tr>
<tr>
<td></td>
<td></td>
<td>EQ1xxx, ER1xxx*</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>digital input</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP2xxx, EQ2xxx, ER2xxx*</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>digital output</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP23xx, EQ23xx, ER23xx*</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>digital combi</td>
<td></td>
</tr>
<tr>
<td>Embedded PCs</td>
<td>CXxxxx</td>
<td>EL1xxx</td>
<td>EJ2xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>digital input</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EL2xxx</td>
<td>EJ4xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>digital output</td>
<td></td>
</tr>
<tr>
<td>Software PLC/Motion Control</td>
<td>TwinCAT</td>
<td>EL3xxx</td>
<td>EJ3xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>analog input</td>
<td></td>
</tr>
<tr>
<td></td>
<td>TwinSAFE</td>
<td>EL4xxx</td>
<td>EJ4xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>analog output</td>
<td></td>
</tr>
<tr>
<td>Safety</td>
<td></td>
<td>EL5xxx</td>
<td>EJ5xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>position measurement</td>
<td></td>
</tr>
<tr>
<td>Redundancy</td>
<td>TwinCAT</td>
<td>EL6xxx</td>
<td>EJ6xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>communication</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EL7xxx</td>
<td>EJ7xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>motion</td>
<td></td>
</tr>
<tr>
<td>System terminals</td>
<td>EL9xxx</td>
<td>EP5xxx</td>
<td>EJ7xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>position measurement</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP6xxx</td>
<td>EJ7xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>communication</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP7xxx</td>
<td>EJ7xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>motion</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>EP8xxx</td>
<td>EJ7xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>multi-functional I/O box</td>
<td></td>
</tr>
</tbody>
</table>

**Couplers**
- EK1xxx: EtherCAT Coupler E-bus
- BK1xxx: EtherCAT Coupler K-bus
- EKxxxx: Bus Coupler for EtherCAT Terminals
- EL1xxx, ES1xxx: digital input
- EL2xxx, ES2xxx: digital output
- EL3xxx, ES3xxx: analog input
- EL4xxx, ES4xxx: analog output
- EL5xxx, ES5xxx: position measurement
- EL6xxx, ES6xxx: communication
- EL7xxx, ES7xxx: motion
- EL9xxx, ES9xxx: system terminals

**Digital I/O**
- EP1xxx, EQ1xxx, ER1xxx*: digital input
- EP2xxx, EQ2xxx, ER2xxx*: digital output
- EP23xx, EQ23xx, ER23xx*: digital combi
- EP3xxx, EQ3xxx, ER3xxx*: analog input
- EP4xxx, ER4xxx*: analog output
- EP5xxx, ER5xxx*: position measurement
- EP6xxx, ER6xxx*: communication
- EP7xxx, ER7xxx*: motion
- EP8xxx, ER8xxx*: multi-functional I/O box

**Analog I/O**
- EP9xx, ER9xx*

**Special functions**
- EP6xxx, ER6xxx*
- EP7xxx, ER7xxx*
- EP8xxx, ER8xxx*
- EP9xx, ER9xx*

**System modules**
- EP1111: EtherCAT Box with ID switch
- EP1122: 2-port EtherCAT junction
- EP9214, EP9224: 4/4-channel power distribution for EtherCAT Box modules

---


---

*We reserve the right to make technical changes.*
### Fieldbus Box

<table>
<thead>
<tr>
<th>Fieldbus Box</th>
<th>Infrastructure Components</th>
<th>Drive Technology</th>
</tr>
</thead>
<tbody>
<tr>
<td>IL230x-B110</td>
<td>PCI Ethernet</td>
<td>Servo Drives</td>
</tr>
<tr>
<td></td>
<td>FC9001, FC9011</td>
<td>AX51xx</td>
</tr>
<tr>
<td></td>
<td>1-channel PCI Ethernet card</td>
<td>EtherCAT Servo Drives up to 170 A, 1-channel</td>
</tr>
<tr>
<td></td>
<td>FC9002</td>
<td>AX52xx</td>
</tr>
<tr>
<td></td>
<td>2-channel PCI Ethernet card</td>
<td>EtherCAT Servo Drives up to 2 x 6 A, 2-channel</td>
</tr>
<tr>
<td></td>
<td>FC9004</td>
<td>AX8000</td>
</tr>
<tr>
<td></td>
<td>4-channel PCI Ethernet card</td>
<td>multi-axis servo system for OCT motors</td>
</tr>
<tr>
<td></td>
<td>FC9051, FC9151</td>
<td>EL7201</td>
</tr>
<tr>
<td></td>
<td>1-channel Mini PCI Ethernet card</td>
<td>servomotor terminal, 50 V DC, 4 A</td>
</tr>
<tr>
<td></td>
<td>FC9022</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2-channel Gbit PCI Ethernet card</td>
<td></td>
</tr>
<tr>
<td></td>
<td>FC9024</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4-channel Gbit PCI Ethernet card</td>
<td></td>
</tr>
<tr>
<td>FM33xx-B110</td>
<td>PCI EtherCAT</td>
<td>Servomotors</td>
</tr>
<tr>
<td></td>
<td>FC1100</td>
<td>AM80xx</td>
</tr>
<tr>
<td></td>
<td>PCI EtherCAT slave card</td>
<td>Synchronous Servomotors with One Cable Technology (OCT)</td>
</tr>
<tr>
<td></td>
<td>FC1121</td>
<td>AM85xx</td>
</tr>
<tr>
<td></td>
<td>PCI Express EtherCAT slave card</td>
<td>Synchronous Servomotors with increased rotor moment of inertia and One Cable Technology (OCT)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>AM88xx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>stainless steel Synchronous Servomotors with One Cable Technology (OCT)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>AM3xxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Synchronous Servomotors</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ALxxxx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Linear Servomotors</td>
</tr>
<tr>
<td></td>
<td>CU2508</td>
<td></td>
</tr>
<tr>
<td></td>
<td>real-time Ethernet port multiplier</td>
<td></td>
</tr>
<tr>
<td></td>
<td>CU1128</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT junction</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EP9128</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT junction,</td>
<td></td>
</tr>
<tr>
<td></td>
<td>8-channel EtherCAT M8, IP 67</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Media converters</td>
<td></td>
</tr>
<tr>
<td></td>
<td>CU1521-0000</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT media converter</td>
<td></td>
</tr>
<tr>
<td></td>
<td>fibre optic ( multimode)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>CU1521-0010</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT media converter</td>
<td></td>
</tr>
<tr>
<td></td>
<td>fibre optic ( singlemode)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>CU1561</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT media converter</td>
<td></td>
</tr>
<tr>
<td></td>
<td>plastic optical fibre</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EP952x</td>
<td></td>
</tr>
<tr>
<td></td>
<td>EtherCAT media converter</td>
<td></td>
</tr>
<tr>
<td></td>
<td>fibre optic ( IP 67)</td>
<td></td>
</tr>
</tbody>
</table>

We reserve the right to make technical changes.
# EtherCAT – Ultra high-speed for automation

## Highlights
- Ethernet up to the terminal – complete continuity
- Ethernet process interface scalable from 1 bit to 64 kbyte
- first true Ethernet solution for the field level
- exact timing and adapted to synchronisation

## Performance
- 256 digital I/Os in 12 µs
- 1,000 digital I/Os in 30 µs
- 200 analog I/Os (16 bit) in 50 µs, corresponding to 20 kHz sampling rate
- 100 servo axes every 100 µs
- 12,000 digital I/Os in 350 µs

## Topology
- line, tree or star topology
- up to 65,535 devices within one network
- network size: almost unlimited (> 500 km)
- operation with or without switches
- cost-effective cabling: Industrial Ethernet patch cable (CAT 5)
  - physical layer:
    - Ethernet 100BASE-TX via twisted pair, up to 100 m between 2 slaves
    - Ethernet 100BASE-FX via fibre optic cable, up to 20 km between 2 slaves
  - hot connect of bus segments

## Address space
- network-wide process image: 4 Gbyte
- device process image: 1 bit to 64 kbyte
- address allocation: freely configurable
- device address selection: automatically via software

## Cost benefits
- no more network tuning: lower engineering costs
- hard real-time with software master: no plug-in cards required
- no active infrastructure components (switches, etc.) required
- Ethernet cable and connector costs lower than for traditional fieldbuses
- EtherCAT down to the I/O terminal: no complex Bus Couplers
- low interface costs due to highly integrated EtherCAT Slave Controller

## Protocol
- optimised protocol directly within the Ethernet frame
- fully hardware-implemented
- for routing and socket interface: UDP datagram
- processing while passing
- distributed clocks for accurate synchronisation
- timestamp data types for resolution in the nanosecond range
- oversampling data types for high-resolution measurements

## Diagnostics
- breaking point detection
- continuous “quality of line” measurement enables accurate localisation of transmission faults
- Topology View

## Interfaces
- switch port terminal for standard Ethernet devices
- fieldbus terminals for fieldbus devices
- decentralised serial interfaces
- communication gateways
- gateway to other EtherCAT systems

## Openness
- fully Ethernet-compatible
- operation with switches and routers possible
- mixed operation with other protocols also possible
- internet technologies (Web server, FTP, etc.)
- compatible with the existing Bus Terminal range
- protocol is published completely
- EtherCAT is IEC, ISO and SEMI standard.

## EtherCAT Technology Group
- international pool of companies
- includes users and manufactures
- supports technology development
- ensures interoperability
- integration and development of device profiles

---

We reserve the right to make technical changes.
Real-time Ethernet: Ultra high-speed right up to the terminal

Outstanding performance, flexible topology and simple configuration characterise EtherCAT (Ethernet for Control Automation Technology), the real-time Ethernet technology from Beckhoff. EtherCAT sets standards where conventional fieldbus systems reach their limits: 1,000 distributed I/Os in 30 µs, almost unlimited network size, and optimum vertical integration thanks to Ethernet and Internet technologies. With EtherCAT, the costly Ethernet star topology can be replaced with a simple line or tree structure – no expensive infrastructure components are required. All types of Ethernet devices can be integrated via a switch port.

Where other real-time Ethernet approaches require special master hardware or scanner cards, EtherCAT manages with very cost-effective standard Ethernet interface cards in the master.

Principle of operation

There are many different approaches that try to provide real-time capability for Ethernet: for example, the CSMA/CD access procedure is disabled via higher level protocol layers and replaced by time slicing or polling. Other propositions use special switches that distribute Ethernet telegrams in a precisely controlled timely manner. While these solutions are able to transport data packets more or less quickly and accurately to the connected Ethernet node, bandwidth utilisation is very poor, particularly for typical automation devices, since even for very small data quantities a complete Ethernet frame has to be sent. Moreover, the times required for the redirection to the outputs or drive controllers and for reading the input data strongly depend on the implementation. A sub-bus is usually also required, particularly in modular I/O systems, which, like the Beckhoff K-bus, may be synchronised and fast, but nevertheless always adds small delays to the communication that cannot be avoided.

With EtherCAT technology, Beckhoff overcomes these system limitations of other Ethernet solutions: the process no longer involves consecutive steps for receiving and interpreting telegrams and copying the process data. In each device (down to the individual terminals) the EtherCAT Slave Controller reads the data relevant for the device while the frame passes through it. Similarly, input data is inserted into the data stream on the fly. While the frames (delayed by only a few bit times) are already passed on, the slave recognises relevant commands and executes them accordingly. The process is hardware-implemented in the slave controller and is, therefore, independent of the protocol stack software runtimes or the processor power. The last EtherCAT slave in the segment returns the fully processed frame, so that the first slave device forwards it to the master as a kind of response telegram.

From an Ethernet point of view, an EtherCAT bus segment is simply a single large Ethernet device that receives and sends Ethernet frames. However, the “device” does not contain a single Ethernet controller with downstream microprocessor, but a large number of EtherCAT slaves. Like for any other Ethernet device, direct communication may be established without a switch, thereby creating a pure EtherCAT system.

Ethernet down to the terminal

The Ethernet protocol remains intact right down to the individual devices, i.e. down to the individual terminals; no sub-bus is required. Only the physical layer is converted in the coupler from 100BASE-TX or -FX to E-bus, in order to meet the requirements of the electronic terminal block. The E-bus signal type (LVDS) within the terminal block is nothing proprietary, it is also used for 10 Gbit Ethernet. At the end of the terminal block, the physical bus characteristics are converted back to the 100BASE-TX standard.

The on-board Ethernet MAC is sufficient as hardware in the master device. DMA (direct memory access) is used for data transfer to the main memory. That means that the network data access burden is lifted from the CPU. The same principle is also used in the Beckhoff multiport cards, which bundle up to four Ethernet channels on one PCI slot.
**EtherCAT Slave Controller (ESC)** | EtherCAT is not only faster outside the I/O device, but also inside. Digital I/Os are directly operated by the EtherCAT Slave Controller, without delays through local firmware and independent of the installed µC performance.

**Protocol**

The EtherCAT protocol is optimised for process data and is either transported directly in the Ethernet frame or packed into UDP/IP datagrams. The UDP version is used in situations where EtherCAT segments in other subnets are addressed via routers. Ethernet frames may contain several EtherCAT telegrams, with each telegram serving a particular memory area of the logical process image with an addressable size of up to 4 GB. The data sequence is independent of the physical order of the EtherCAT Terminals in the network; addressing can be in any order. Broadcast, Multicast and communication between slaves are possible.

The protocol can also handle parameter communication, which typically is acyclical. The structure and meaning of the parameters is specified via CANopen device profiles, which are available for a wide range of device classes and applications. EtherCAT also supports the SERCOS servo profile according to IEC 61800-7-204.

In addition to data exchange according to the master/slave principle, EtherCAT is also very suitable for communication between controllers (master/master). Freely addressable network variables for process data and a variety of services for parameterisation, diagnosis, programming and remote control cover a wide range of requirements. The data interfaces for master/slave and master/master communication are identical.

**Performance**

EtherCAT reaches new dimensions in network performance. The update time for the data from 1,000 distributed inputs/outputs is only 30 µs – including terminal cycle time. Up to 1,486 byte of process data can be exchanged with a single Ethernet frame – this is equivalent to almost 12,000 digital inputs and outputs. The transfer of this data quantity only takes 300 µs.

The communication with 100 servo axes takes place every 100 µs. With this cycle time, all axes are provided with set values and control data and report their actual position and status. The distributed clocks technique enables the axes to be synchronised with a jitter of significantly less than 1 microsecond.

The extremely high performance of the EtherCAT technology enables control concepts that could not be realised with classic fieldbus systems. Very fast control loops can thus also be closed via the bus. Functions that previously required dedicated local hardware support can now be mapped in software. The tremendous bandwidth enables status information to be transferred with each data item. With EtherCAT, a communication technology is available that matches the superior computing capacity of modern Industrial PCs. The bus system is no longer the “bottleneck” of the control concept. Distributed I/Os are recorded faster than is possible with most local I/O interfaces.

The benefits of this network performance also become apparent in smaller controllers with comparatively moderate computing capacity. The EtherCAT cycle is so fast that it can be executed between two control cycles. The controller thus always has the latest input data available; the outputs are addressed with minimum delay. The response behaviour of the controller is improved significantly without increasing the computing capacity itself.

The EtherCAT technology principle is scalable and not bound to the baud rate of 100 Mbaud – extension to Gbit Ethernet is possible.
Freedom in the choice of topology | Maximum flexibility for wiring: with or without switch, line or tree topologies can be freely selected and combined. Address assignment is automatic; no IP address setting is required.

EtherCAT instead of PCI
With increasing miniaturisation of the PC components, the physical size of Industrial PCs is increasingly determined by the number of required slots. The bandwidth of Fast Ethernet, together with the data width of the EtherCAT communication hardware (EtherCAT Slave Controller) enables the transfer of PC interfaces to intelligent interface terminals at the EtherCAT system. Apart from the decentralised I/Os, axes and control units, complex systems such as fieldbus masters, fast serial interfaces, gateways and other communication interfaces can be addressed via a single Ethernet port in the PC. Even further Ethernet devices without restriction on protocol variants can be connected via decentralised switch port terminals. The central IPC becomes smaller and therefore more cost-effective, one Ethernet interface is sufficient for the complete communication with the periphery.

Topology
Line, tree or star: EtherCAT supports almost any topology. The bus or line structure known from the fieldbuses thus also becomes available for Ethernet. Particularly useful for system wiring is the combination of lines and branches or stubs. The required interfaces exist on the couplers; no additional switches are required. Naturally, the classic Ethernet star topology with junction terminals can also be used.

Wiring flexibility is further maximised through the choice of different cables. Flexible and inexpensive shielded Industrial Ethernet fieldbus cables transfer the signals in Ethernet mode (100BASE-TX) up to a cable length of 100 m between two devices. The complete bandwidth of the Ethernet network — such as different optical fibres and copper cables — can be used in combination with switches or media converters. For each cable distance, the signal variant can be selected individually. Since up to 65,535 devices can be connected, the size of the network is almost unlimited.

Distributed clocks
Accurate synchronisation is particularly important in cases where spatially distributed processes require simultaneous actions. This may be the case, for example, in applications where several servo axes carry out coordinated movements simultaneously.

The most powerful approach for synchronisation is the accurate alignment of distributed clocks. In contrast to fully synchronous communication, where synchronisation quality suffers immediately in the event of a communication fault, distributed aligned clocks have a high degree of tolerance vis-à-vis possible fault-related delays within the communication system. With EtherCAT, the data exchange is fully based on a pure hardware machine. Since the communication utilises a logical (and thanks to full-duplex Fast Ethernet, also physical) ring structure, the reference clock can determine the runtime offset to the individual local clocks simply and accurately — and vice versa. The distributed clocks are adjusted based on this value, which means that a very precise network-wide timebase with a jitter of significantly less than 1 microsecond is available.

However, high-resolution distributed clocks are not only used for synchronisation, but can also provide accurate information about the local timing of the data acquisition. Thanks to extended data types, very precise time stamps can be assigned to measured values.

Hot Connect
Many applications require a change in I/O configuration during operation. Examples are machining centres with changing, sensor-equipped tool systems or transfer systems with intelligent, flexible workpiece carriers, or printing machines in which individual printing units are switched off. The protocol structure of the EtherCAT system takes account of these requirements: the Hot Connect function enables parts of the network to be linked and decoupled or reconfigured "on the fly", offering flexible response capability for changing configurations.

High availability
Increasing demands in terms of system availability are catered for with optional
Distributed clocks | Local absolute system synchronisation for CPU, I/O and drive units

Cable redundancy that enables devices to be changed without having to shut down the network. EtherCAT also supports redundant masters with hot standby functionality. Since the EtherCAT Slave Controllers immediately return the frame automatically if an interruption is encountered, failure of a device does not lead to the complete network being shut down. Dragchain applications, for example, can thus be specifically configured as stubs in order to be prepared for cable break.

Safety over EtherCAT
In the interest of achieving safe data communication with EtherCAT, the Safety over EtherCAT protocol has been created. The protocol meets the requirements of IEC 61508 up to Safety Integrity Level (SIL) 3 and IEC 61784-3, as approved by the German Technical Inspection Agency (TÜV).

EtherCAT is used as a single-channel communication system. The transport medium is regarded as a “black channel” and is not included in the safety considerations. Thus, the protocol can also be transmitted by other communication systems, backplanes, WLAN, etc. The transfer cycle can be as short as required without affecting residual error probability. The cyclic exchange of safe data between a Safety over EtherCAT master and a Safety over EtherCAT slave is referred to as a connection that is monitored via a watchdog timer. A master can establish and monitor several connections to different slaves.

Diagnostics
The diagnostic capability of a network is a crucial factor for availability and commissioning times — and therefore overall costs. Only faults that are detected quickly and accurately and located unambiguously can be rectified quickly. Therefore, special attention was paid to comprehensive diagnostic features during the development of EtherCAT.

During commissioning, the actual configuration of the I/O terminals should be checked for consistency with the specified configuration. The topology should also match the configuration. Due to the built-in topology recognition down to the individual terminals, the verification can not only take place during system start-up, automatic reading in of the network is also possible (configuration upload).

Bit faults during the transfer are reliably detected through evaluation of the CRC checksum in each device. Apart from breaking point detection and localisation, the protocol, transfer physics and topology of the EtherCAT system enable individual quality monitoring of each individual transmission segment. The automatic evaluation of the associated error counters enables precise localisation of critical network sections. Gradual or changing sources of error such as EMC influences, defective connectors or cable damage are detected and located.

EtherCAT components
On the hardware side, EtherCAT technology is located in EtherCAT Terminals, for example. The I/O system in protection class IP 20 is based on the housing of the tried and tested Beckhoff Bus Terminal system. In contrast to Bus Terminals, where the fieldbus protocol data is converted within the Bus Coupler to the internal, fieldbus-independent terminal bus, the EtherCAT protocol remains fully intact down to the individual terminal. In addition to EtherCAT Terminals with E-bus connection, the proven standard Bus Terminals with K-bus connection can also be connected via the BK1120 EtherCAT Bus Coupler. This ensures compatibility and continuity with the prevalent system. Existing and future investments are protected.

EtherCAT is fully integrated into the Beckhoff control architecture. The EtherCAT Box modules feature an integrated EtherCAT interface and can be connected directly to an EtherCAT network without an additional Coupler Box. The EPxxxx series with industrial housing and protection class IP 67 is suitable for application directly at the machine in harsh industrial environments. The EQxxxx series with stainless steel housing and protection class IP 69K is suitable for applications with high hygienic standards, such as in the food, chemical or pharmaceutical industries.

The Beckhoff Industrial PCs, the Embedded PCs of the CX series, the Control Panels
**Protocol structure** | The process image allocation is freely configurable. Data are copied directly in the I/O terminal to the desired location within the process image: no additional mapping is required. There is a very large address space of 4 Gbytes.

with control functionality, and the Ethernet PCI cards already offer inherent EtherCAT capability. The Beckhoff Servo Drives are also available with EtherCAT interface.

**Openness**

The EtherCAT technology is not only fully Ethernet-compatible, but also characterised by particular openness “by design”: the protocol tolerates other Ethernet-based services and protocols on the same physical network – usually only with minimum loss of performance. Any Ethernet device can be connected within the EtherCAT segment via a switch port terminal without influencing the cycle time. Devices with fieldbus interface are integrated via EtherCAT fieldbus master terminals. The UDP protocol variant can be implemented on each socket interface. EtherCAT is a fully open protocol. It is recognised and available as an official IEC specification (IEC 61158, type 12).

**EtherCAT Technology Group**

The EtherCAT Technology Group (ETG) is an association of automation users and manufacturers with a mission to support the development of EtherCAT technology. The group represents a variety of industry sectors and application areas. This ensures that the EtherCAT technology functions and interfaces are ideally prepared for the widest range of applications. The organisation ensures that EtherCAT can be easily and cost-effectively integrated in all kinds of automation devices, while ensuring interoperability of these implementations. The EtherCAT Technology Group is the official IEC partner organisation for fieldbus standardisation. Membership is open to all companies.

For further information see [www.ethercat.org](http://www.ethercat.org)
EtherCAT system overview

Flexible topology

- Ethernet TCP/IP
- Automation suite, EtherCAT master
- Bus/line
- Tree/star
- Industrial Ethernet cable (100BASE-TX)
- 100 m
- Fibre optic (100BASE-FX)
- 50 m (POF)
- 2,000 m (multimode)
- 20,000 m (singlemode)
- E-bus
- K-bus
- BK1250

Fieldbus integration

- CANopen
- DeviceNet
- Ethernet
- EtherCAT
- RS232
- RS485
- IO-Link
- IEEE1588

We reserve the right to make technical changes.
Safety inputs/outputs

TwinSAFE Logic

EtherCAT bridge

Cable redundancy

EL6695

Safety

Safety inputs/outputs

Safety Drives with TwinSAFE option cards

We reserve the right to make technical changes.
The I/O response time includes all hardware processing times (IPC, EtherCAT and I/O system), ranging from physical input event to output response. With an I/O response time of < 100 µs, PLC programmers have access to performance that in the past was only available in servo controllers with digital signal processors.
EtherCAT | Even faster with XFC

With XFC technology (xXtreme Fast Control) Beckhoff presents an ultra fast control solution: XFC is based on optimised control and communication architectures comprising an advanced Industrial PC, ultra-fast I/O terminals with extended real-time characteristics, the EtherCAT high-speed Ethernet system, and the TwinCAT automation software. With XFC it is possible to achieve I/O response times < 100 µs. This technology opens up new process optimisation opportunities for the user that were not possible in the past due to technical limitations.

XFC represents a control technology that enables very fast and highly deterministic responses. It includes all hardware and software components involved in control applications: optimised input and output components that can detect signals with high accuracy or initiate tasks; EtherCAT as very fast communication network; high-performance Industrial PCs; and TwinCAT, the automation software that links all system components.

Not long ago, control cycle times around 10 to 20 ms were normal. The communications interface was free-running, with corresponding inaccuracy of the determinism associated with responses to process signals. The increased availability of high-performance Industrial PC controllers enabled a reduction in cycle times down to 1–2 ms, i.e. by about a factor of 10. Many special control loops could thus be moved to the central machine controller, resulting in cost savings and greater flexibility in the application of intelligent algorithms.

XFC offers a further reduction of response times by a factor of 10, and enables cycle times of 100 µs and below, without having to give up central intelligence and associated high-performance algorithms. XFC also includes additional technologies that not only improve cycle times, but also temporal accuracy and resolution.

Users benefit from options for enhancing the quality of their machines and reducing response times. Measuring tasks such as preventive maintenance measures, monitoring of idle times or documentation of parts quality can simply be integrated in the machine control without additional, costly special devices.

In a practical automation solution, not everything has to be extremely fast and accurate – many tasks can still be handled with “normal” solutions. XFC technology is therefore fully compatible with existing solutions and can be used simultaneously with the same hardware and software.

TwinCAT – The extreme fast real-time control software
- real-time under Microsoft Windows down to 12.5 µs cycle time
- standard IEC 61131-3 programming in XFC real-time tasks
- Standard features of Windows and TwinCAT are XFC-compliant.

EtherCAT – The extreme fast control communication technology
- 1,000 distributed digital I/Os in 30 µs
- EtherCAT down to the individual I/O terminals, no sub bus required
- optimised use of standard Ethernet Controllers, e.g. Intel® PC chipset architecture in the EtherCAT master
- advanced real-time feature based on distributed clocks
  - synchronisation
  - time stamping
  - oversampling

EtherCAT Terminals – The extreme fast I/O technology
- full range I/O line for all signal types
- high-speed digital and analog I/Os
- Time stamping and oversampling features allow extreme high timing resolution (down to 10 ns).

IPC – The extreme fast control CPU
- Industrial PC based on high-performance real-time motherboards
- compact form factors optimised for control applications
XFC technologies

Distributed clocks

In a normal, discrete control loop, actual value acquisition occurs at a certain time (input component), the result is transferred to the control system (communication component), the response is calculated (control component), the result is communicated to the set value output module (output component) and issued to the process (controlled system).

The crucial factors for the control process are: minimum response time, deterministic actual value acquisition (i.e. exact temporal calculation must be possible), and corresponding deterministic set value output. At what point in time the communication and calculation occurs in the meantime is irrelevant, as long as the results are available in the output unit in time for the next output, i.e. temporal precision is required in the I/O components, but not in the communication or the calculation unit.

The distributed EtherCAT clocks therefore represent a basic XFC technology and are a general component of EtherCAT communication. All EtherCAT devices have their own local clocks, which are automatically and continuously synchronised with all other clocks via the EtherCAT communication. Different communication runtimes are compensated, so that the maximum deviation between all clocks is generally less than 100 nanoseconds. The current time of the distributed clocks is therefore also referred to as system time, because it is always available across the whole system.

Timestamp/multi-timestamp

Process data is usually transferred in its respective data format (e.g. one bit for a digital value or one word for an analog value). The temporal relevance of the process record is therefore inherent in the communication cycle during which the record is transferred. However, this also means that the temporal resolution and accuracy is limited to the communication cycle.

Timestamped data types contain a timestamp in addition to their user data. This timestamp – naturally expressed in the ubiquitous system time – enables provision of temporal information with significantly higher precision for the process record. Timestamps can be used for inputs (e.g. to identify the time of an event occurred) and outputs (e.g. timing of a response). This way it is possible to determine, for example, the precise point in time when an output is to be switched. The switching task is executed independently of the bus cycle.

While timestamp terminals can execute one switching task or switching event per bus cycle, multi-timestamp terminals can execute up to 32 switching tasks or switching events per cycle.

Signal technology for terminals with timestamping (64 bit time resolution)
- extremely precise time measurement for digital single shot events per cycle: resolution 1 ns, internal sampling 10 ns, accuracy with distributed clocks << 1 μs (+ input delay)
- exact time measurement of rising or falling edges of distributed digital inputs
- exact timing of distributed output signals, independent of control cycle
- absolute distributed clocks time with 64 bit resolution, easy time handling over > 580 years

Signal technology for terminals with multi-timestamping (32 bit time resolution)
- precise time measurement of up to 32 events per cycle: resolution 1 ns, internal sampling 10…40 μs dependent on the configuration
- exact time measurement of rising or falling edges of distributed digital inputs
- exact timing of distributed output signals, independent of control cycle
- distributed clocks time with 32 bit resolution, sufficient for actions in a ±4-second time frame
Oversampling

Process data is usually transferred exactly once per communication cycle. Conversely, the temporal resolution of a process record directly depends on the communication cycle time. Higher temporal resolution is only possible through a reduction in cycle time – with associated practical limits.

Oversampling data types enable multiple sampling of a process record within a communication cycle and subsequent (inputs) or prior (outputs) transfer of all data contained in an array. The oversampling factor describes the number of samples within a communication cycle and is therefore a multiple of one. Sampling rates of 200 kHz can easily be achieved, even with moderate communication cycle times.

Triggering of the sampling within the I/O components is controlled by the local clock (or the global system time), which enables associated temporal relationships between distributed signals across the whole network.

Fast I/O

Very fast physical responses require suitably short control cycle times in the associated control system. A response can only take place once the control system has detected and processed an event.

The traditional approach for achieving cycle times in the 100 µs range relies on special separate controllers with their own, directly controlled I/Os. This approach has clear disadvantages, because the separate controller has only very limited information about the overall system and therefore cannot make higher-level decisions. Reparameterisation options (e.g. for new workpieces) are also limited. Another significant disadvantage is the fixed I/O configuration, which generally cannot be expanded.

Signal oversampling
- multiple signal conversion within one control cycle
- hard time synchronisation through distributed clocks
- for digital input/output signals
- for analog input/output signals
- support of analog I/O EtherCAT Terminals
  - up to 100 kHz signal conversion
  - down to 10 µs time resolution
- support of digital I/O EtherCAT Terminals
  - up to 1 MHz
  - up to 1 µs time resolution
- application
  - fast signal monitoring
  - fast function generator output
  - signal sampling independent of cycle time
  - fast loop control

Extremefast I/O response time
- from 85 µs
- Deterministic synchronised input and output signal conversion leads to low process timing jitter.
- Process timing jitter is independent of communication and CPU jitter.

Extreme short control cycle time
- 100 µs (min. 12.5 µs)
- new performance class for PLC application:
  - control loops with 100 µs

We reserve the right to make technical changes.
XFC components

Implementation of the XFC technologies described above requires full support for all hardware and software components involved in the control system, including fast, deterministic communication and I/O and control hardware. A significant part of XFC are the software components responsible for fast processing of the control algorithms and optimised configuration of the overall system.

Beckhoff offers a special XFC product range based primarily on four categories: EtherCAT as fieldbus, EtherCAT Terminals as I/O system, IPCs as hardware platform, and TwinCAT as higher-level software. All components are based on open standards, which means that any engineer or programmer can develop very fast control solutions with high performance based on standard components (i.e. without special hardware).

I/O component with XFC technology
Standard EtherCAT Terminals already offer full support for XFC technology. Synchronisation of the I/O conversion with the communication or – more precisely – with the distributed clocks is already standard in EtherCAT and is therefore supported by the corresponding terminals.

XFC terminals offer additional special features that make them particularly suitable for fast or high-precision applications:
- digital EtherCAT Terminals with very short TON/TOFF times, or analog terminals with particularly short conversion times
- EtherCAT Terminals and EtherCAT Box modules with timestamp/multi-time-stamping latching at the exact system time at which digital or analog events occur. Output of digital or analog values can occur at exactly predefined times.
- Terminals with oversampling enable actual value acquisition or set value output with significantly higher resolution than the communication cycle time.

Communication component – EtherCAT fully utilised
With high communication speed and usable data rates EtherCAT offers the basic prerequisites for XFC. However, speed is not everything. The option of using the bus to exchange several independent process images arranged according to the control application enables parallel application of XFC and standard control technology. The central control system is relieved of time-consuming copying and mapping tasks and can fully utilise the available computing power for the control algorithms.

The distributed EtherCAT clocks that form the temporal backbone of the XFC technologies are available in all communication devices without significant additional effort.

The crucial point of XFC is the option of integrating all I/O components directly in the EtherCAT communication, so that no subordinate communication systems (sub bus) are required. In many XFC terminals the AD or DA converter is connected directly to the EtherCAT chip, so that delays are avoided.

Control component – High-performance Industrial PCs
Central control technology can be particularly advantageous if it can run faster and more powerful control algorithms than would be the case with many distributed small controllers.

Fast multi-core processors are ideal for running the operator interface of the machine in parallel with the control tasks. Large caches available with modern CPUs are ideal for XFC technology, because fast algorithms run in the cache and can therefore be processed even faster.

An important factor for short XFC cycle times is the fact that the CPU is not burdened with complex process data copying tasks needed by traditional fieldbuses with their DPRAM-based central boards. EtherCAT process data communication can be handled entirely by the integrated Ethernet controller (NIC with bus master DMA).

Software component – TwinCAT automation suite
TwinCAT as high-performance automation suite fully supports the XFC technologies while retaining all the familiar features. The real-time implementation of TwinCAT supports different tasks with different cycle times. Modern Industrial PCs can achieve cycle times of 100 µs or less without problem. Several (different) fieldbuses can be mixed. The associated allocations and communication cycles are optimised according to the fieldbus capabilities. The EtherCAT implementation in TwinCAT makes full use of the communication system and enables application of several independent time levels. It uses distributed clocks. Different time levels enable coexistence of XFC and normal control tasks in the same system, without the XFC requirements becoming a "bottleneck".

An option specially designed for XFC enables inputs to be read during independent communication calls and outputs to be sent directly after the calculation. Due to the speed offered by EtherCAT the inputs are read and processed “just” before the start of the control tasks, followed by immediate distribution of the outputs with a second fieldbus cycle. The resulting response times are faster than the fieldbus cycle time in some cases.

Special TwinCAT extensions facilitate handling of the XFC data types (timestamp and oversampling). PLC blocks enable simple analysis and calculation of the time stamps. The TwinCAT scope can display the data picked up via oversampling according to the allocated oversampling factor and enables precise data analyses.
**Oversampling**
Oversampling offers refined temporal resolution of a signal through multiple signal sampling.

**Industrial PC**
High-performance Industrial PCs offer plenty of computing power for short XFC cycle times.

**EtherCAT**
EtherCAT offers the basis for XFC with an extremely fast communication technology.

**TwinCAT**
The TwinCAT automation suite supports XFC technology with real-time implementation and extensions for the XFC functions oversampling, timestamp and distributed clocks.

**Timestamp**
Timestamp input/output modules can be used to realise responses with equidistant time intervals.

**Fast I/O**
Fast I/O make delays in the hardware negligible.

**Drive Technology**
The flexible drive interface with short cycle times enables highly dynamic, strictly synchronous control processes covering multiple axes.

We reserve the right to make technical changes.
We reserve the right to make technical changes.
ET1100, ET1200 | EtherCAT ASICs

The ET1100 and ET1200 EtherCAT ASICs offer a cost-effective and compact solution for realising EtherCAT slaves. They process the EtherCAT protocol in the hardware and therefore ensure high-performance and real-time capability, independent of any downstream slave microcontrollers and associated software. Through their three process data interfaces – digital I/O, SPI and 8/16 bit µC (not for ET1200) – the EtherCAT ASICs enable realisation of simple digital modules without microcontrollers and development of intelligent devices with own processor. Both ASICs feature distributed clocks that enable high-precision synchronisation (<< 1 µs) of the EtherCAT slaves. The supply voltage is 3.3 V or 5 V; the core voltage of 2.5 V is generated by the integrated in-phase regulator or can be supplied directly. The ET1100 is suitable as a universal solution for all types of EtherCAT devices; the ET1200 is optimised for modular devices using E-bus/LVDS (Low Voltage Differential Signalling) as internal interface. Due to their compact design and small number of external components, both ASICs only require minimum space on the board.

The ET1100 ASIC housing (BGA128) only measures 10 x 10 mm. The chip can support up to four EtherCAT ports. The 8 kB internal memory (DPRAM) for access to process and parameter data is optionally addressed via parallel or serial data bus. Alternatively, the ASICs can also be used without controller. In this case up to 32 digital signals can be connected directly.

The ET1200 ASIC is the “small” variant of the ET1100; with its QFN48 housing measuring only 7 x 7 mm, the chip is even more compact. The device offers 16 digital I/O interfaces and distributed clock hardware for high-precision synchronisation. The 1 kB internal DPRAM is addressed via a fast (20 Mbit/s) serial interface. The “small ASIC” offers up to three EtherCAT ports, one of which can be used as MII for connecting a standard PHY. The other ports are used for LVDS, which makes the ET1200 the right choice for modular devices using LVDS as internal bus physics.

<table>
<thead>
<tr>
<th>Technical data</th>
<th>ET1100</th>
<th>ET1200</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of EtherCAT ports</td>
<td>4 (max. 4 x MII)</td>
<td>3 (max. 1 x MII)</td>
</tr>
<tr>
<td>FMMUs</td>
<td>8</td>
<td>3</td>
</tr>
<tr>
<td>SYNC manager</td>
<td>8</td>
<td>4</td>
</tr>
<tr>
<td>DPRAM</td>
<td>8 kbyte</td>
<td>1 kbyte</td>
</tr>
<tr>
<td>Distributed clocks</td>
<td>yes (64 bit)</td>
<td>yes (64 bit)</td>
</tr>
<tr>
<td>Process data interfaces</td>
<td>32 bit digital I/O</td>
<td>16 bit digital I/O</td>
</tr>
<tr>
<td></td>
<td>SPI</td>
<td>SPI</td>
</tr>
<tr>
<td></td>
<td>8/16 bit µC</td>
<td>–</td>
</tr>
<tr>
<td>Housing</td>
<td>BGA128, 10 x 10 mm</td>
<td>QFN48, 7 x 7 mm</td>
</tr>
<tr>
<td>Further information</td>
<td>[<a href="http://www.beckhoff.com/ET1100">www.beckhoff.com/ET1100</a>]</td>
<td>[<a href="http://www.beckhoff.com/ET1200">www.beckhoff.com/ET1200</a>]</td>
</tr>
</tbody>
</table>

We reserve the right to make technical changes.
The EtherCAT IP core enables the EtherCAT communication function and application-specific functions to be implemented on an FPGA (Field Programmable Gate Array – i.e. a device containing programmable logical components). The EtherCAT functionality is freely configurable. The IP core can be combined with own FPGA designs, and it can be integrated in System-on-Chips (SoCs) with soft core processors or hard processor systems via the Avalon® or AMBA® AXI™ interfaces. The physical interfaces and internal functions, such as the number of FMMUs and SYNC managers, the size of the DPRAM, etc., are adjustable. The process data interface (PDI) and the distributed clocks are also configurable. The functions are compatible with the EtherCAT specification and the EtherCAT ASICs (ET1100, ET1200).

The ET1811 quantity-based license for Altera® FPGAs offers manufacturers of small lots and development service providers the possibility of entering the world of EtherCAT development with low initial investment. For the development of an EtherCAT device, the ET1811 one-time kick-off charge is required, plus the ET1811-1000 royalty for 1,000 devices. The royalties for 1,000 devices must be paid in advance each time.

For development service providers only the ET1811 one-time kick-off charge will be required; the ET1811-0030 system integrator OEM license will be required for each customer implementation. The end customer will be required to pay the royalty license (ET1811-1000).

<table>
<thead>
<tr>
<th>Configurable features</th>
<th>ET1810, ET1811, ET1812</th>
</tr>
</thead>
<tbody>
<tr>
<td>PHY interface</td>
<td>1…3 ports MII, 1…3 ports RGMII or 1…2 ports RMII</td>
</tr>
<tr>
<td>FMMUs</td>
<td>0…8</td>
</tr>
<tr>
<td>SYNC manager</td>
<td>0…8</td>
</tr>
<tr>
<td>DPRAM</td>
<td>0…60 KB</td>
</tr>
<tr>
<td>Distributed clocks</td>
<td>0…2 SYNC outputs, 0…2 latch inputs (32/64 bit)</td>
</tr>
<tr>
<td>Process data interfaces</td>
<td>32 bit digital I/O, SPI, 8/16 bit asynchronous µC interface, Avalon interface, AMBA AXI3 interface, 64 bit general purpose I/O</td>
</tr>
<tr>
<td>Further information</td>
<td><a href="http://www.beckhoff.com/ET1810">www.beckhoff.com/ET1810</a></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Ordering information</th>
</tr>
</thead>
<tbody>
<tr>
<td>Node-locked buy out license</td>
</tr>
<tr>
<td>ET1810</td>
</tr>
<tr>
<td>ET1810-0010</td>
</tr>
<tr>
<td>ET1810-0020</td>
</tr>
<tr>
<td>Node-locked quantity-based license</td>
</tr>
<tr>
<td>ET1811</td>
</tr>
<tr>
<td>ET1811-1000</td>
</tr>
<tr>
<td>ET1811-0020</td>
</tr>
<tr>
<td>ET1811-0030</td>
</tr>
<tr>
<td>Floating buy out license</td>
</tr>
<tr>
<td>ET1812</td>
</tr>
<tr>
<td>ET1812-0010</td>
</tr>
<tr>
<td>ET1812-0020</td>
</tr>
</tbody>
</table>

Evaluation license (Open Core Plus IP)
Full-featured, time-limited version available. ▶ www.beckhoff.com/ET1810
ET1815, ET1816 | EtherCAT IP core for Xilinx® FPGAs

The EtherCAT IP core enables the EtherCAT communication function and application-specific functions to be implemented on an FPGA (Field Programmable Gate Array – i.e. a device containing programmable logical components). The EtherCAT functionality is freely configurable. The IP core can be combined with own FPGA designs, and it can be integrated in System-on-Chips (SoCs) with soft core processors or hard processing systems via the AMBA® AXI™ interfaces. The physical interfaces and internal functions, such as the number of FMMUs and SYNC managers, the size of the DPRAM, etc., are adjustable. The process data interface (PDI) and the distributed clocks are also configurable. The functions are compatible with the EtherCAT specification and the EtherCAT ASICs (ET1100, ET1200).

The ET1816 quantity-based license offers manufacturers of small lots and development service providers the possibility of entering the world of EtherCAT development with low initial investment. For the development of an EtherCAT device, the ET1816 one-time kick-off charge is required, plus ET1816-1000 royalty for 1,000 devices. The royalties for 1,000 devices must be paid in advance each time.

Development service providers only require ET1816 one-time kick-off charge; the ET1811-0030 system integrator OEM license is required for each customer implementation. The end customer requires the royalty license (ET1816-1000).

<table>
<thead>
<tr>
<th>Configurable features</th>
<th>ET1815, ET1816</th>
</tr>
</thead>
<tbody>
<tr>
<td>PHY interface</td>
<td>1…3 ports MII, 1…3 ports RGMII or 1…2 ports RMII</td>
</tr>
<tr>
<td>FMMUs</td>
<td>0…8</td>
</tr>
<tr>
<td>SYNC manager</td>
<td>0…8</td>
</tr>
<tr>
<td>DPRAM</td>
<td>0…60 KB</td>
</tr>
<tr>
<td>Distributed clocks</td>
<td>0…2 SYNC outputs, 0…2 latch inputs (32/64 bit)</td>
</tr>
<tr>
<td>Process data interfaces</td>
<td>32 bit digital I/O, SPI, 8/16 bit asynchronous µC interface, AMBA AXI4/AXI4 LITE interface, 64 bit general purpose I/O</td>
</tr>
<tr>
<td>Further information</td>
<td><a href="http://www.beckhoff.com/ET1815">www.beckhoff.com/ET1815</a></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Ordering information</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Node-locked buy out license</td>
<td>ET1815</td>
</tr>
<tr>
<td></td>
<td>Node-locked license for using the EtherCAT IP core on one workstation. The license includes 1 year of maintenance and updates.</td>
</tr>
<tr>
<td>ET1815-0010</td>
<td>Extension of the node-locked Xilinx license (ET1815) for one additional workstation</td>
</tr>
<tr>
<td>ET1815-0020</td>
<td>One-year maintenance extension for node-locked license (ET1815)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Node-locked quantity-based license</th>
</tr>
</thead>
<tbody>
<tr>
<td>ET1816</td>
</tr>
<tr>
<td>One-time kick-off charge for the node-locked quantity-based license for using the freely configurable EtherCAT IP cores on one workstation; target hardware: selected Xilinx devices</td>
</tr>
<tr>
<td>ET1816-1000</td>
</tr>
<tr>
<td>Royalty for 1,000 devices, ET1816 required</td>
</tr>
<tr>
<td>ET1816-0020</td>
</tr>
<tr>
<td>One-year maintenance extension, ET1816 required</td>
</tr>
<tr>
<td>ET1811-0030</td>
</tr>
<tr>
<td>System integrator OEM license</td>
</tr>
</tbody>
</table>

Evaluation license
FullFeatured, time-limited version available. ➤ www.beckhoff.com/ET1815

We reserve the right to make technical changes.
EL9820 | EtherCAT evaluation kit

The evaluation kit serves as platform for the development of EtherCAT slaves. The piggyback controller board supplied with the kit realises the complete EtherCAT connection with the ASIC ET1100. All digital I/O, SPI and asynchronous µController process data interfaces (PDIs) are connected to pin strips and can be selected via PDI selector switch. The SPI interface can optionally be connected with a PIC microcontroller included with the kit or directly to the pin strip. A programming and debugging interface for the controller is also provided. The EL9820 can therefore also be used as platform for the ET9300 EtherCAT Slave Stack Code provided with the evaluation kits.

<table>
<thead>
<tr>
<th>Technical data</th>
<th>EL9820</th>
</tr>
</thead>
<tbody>
<tr>
<td>Evaluation kit</td>
<td>base board</td>
</tr>
<tr>
<td>EtherCAT Slave Controller</td>
<td>ASIC ET1100</td>
</tr>
<tr>
<td>EtherCAT piggyback controller board</td>
<td>FB1111-0142 with ASIC ET1100</td>
</tr>
<tr>
<td>Software</td>
<td>EtherCAT Slave Stack Code ET9300</td>
</tr>
<tr>
<td>Accessories</td>
<td>cables, documentation</td>
</tr>
<tr>
<td>Workshop</td>
<td>optionally available as TR8100</td>
</tr>
<tr>
<td>Further information</td>
<td><a href="http://www.beckhoff.com/EL9820">www.beckhoff.com/EL9820</a></td>
</tr>
</tbody>
</table>
FB1111 | EtherCAT piggyback controller boards

The FB1111 EtherCAT piggyback controller board offers complete EtherCAT connection based on the ET1100 EtherCAT ASIC. All variants of the FB1111 have the same form factor and can be used with the EtherCAT evaluation kit. They can be integrated as EtherCAT interfaces in devices.

<table>
<thead>
<tr>
<th>Ordering information</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FB1111-0140</td>
<td>EtherCAT piggyback controller board with ET1100 and µC interface; can be integrated as EtherCAT interface in devices.</td>
</tr>
<tr>
<td>FB1111-0141</td>
<td>EtherCAT piggyback controller board with ET1100 and SPI interface; can be integrated as EtherCAT interface in devices.</td>
</tr>
<tr>
<td>FB1111-0142</td>
<td>EtherCAT piggyback controller board with ET1100 and digital I/O interface; can be integrated as EtherCAT interface in devices; included in the EL9820 evaluation kit.</td>
</tr>
</tbody>
</table>

➡️ www.beckhoff.com/FB1111

We reserve the right to make technical changes.
ET2000 | Industrial Ethernet multi-channel probe

The ET2000 multi-channel probe from Beckhoff is a versatile piece of hardware for analysing any Industrial Ethernet solution. With eight ports this device enables unlimited synchronised recording of up to four independent channels at a speed of 100 Mbit/s. All real-time Ethernet standards such as EtherCAT, PROFINET, etc. and conventional office Ethernet networks are supported.

Through its compact and rugged design the ET2000 is ideal both for the local application at machines or in the laboratory. The four channels enable recording and analysis of separate networks or different points within the same network. All frames in transit – in both directions – are furnished with a high-precision time stamp in the probe hardware and copied to the Gbit uplink port. The high time stamp resolution of 1 ns enables very precise timing analysis of the connected network segments. The ET2000 probe is transparent for the connected buses. Thanks to the low cycle delay of 1 µs the influence on the system is very small.

The device can be connected to any Gbit Ethernet interfaces on the PC side. A plug-in for the free Wireshark network monitor enables this network monitor to be used for analysing recordings and high-precision time stamps.

<table>
<thead>
<tr>
<th>Technical data</th>
<th>ET2000</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of ports/channels</td>
<td>8/4</td>
</tr>
<tr>
<td>Uplink port</td>
<td>1 Gbit/s</td>
</tr>
<tr>
<td>Delay</td>
<td>approx. 1 µs</td>
</tr>
<tr>
<td>Resolution timestamp</td>
<td>1 ns (channel 0/1)</td>
</tr>
<tr>
<td>Software interface</td>
<td>WinPcap</td>
</tr>
<tr>
<td>Data transfer rates</td>
<td>probe ports: 100 Mbit/s, uplink port: 1 Gbit/s</td>
</tr>
<tr>
<td>Hardware diagnosis</td>
<td>link/activity LED per channel, 1 power LED</td>
</tr>
<tr>
<td>Power supply</td>
<td>24 (18…30) V DC, 500 mA, 3-pole terminal (+, -, PE)</td>
</tr>
<tr>
<td>Dimensions (W x H x D)</td>
<td>approx. 100 mm x 150 mm x 40 mm</td>
</tr>
<tr>
<td>Operating temperature</td>
<td>0…+55 °C</td>
</tr>
<tr>
<td>Further information</td>
<td><a href="http://www.beckhoff.com/ET2000">www.beckhoff.com/ET2000</a></td>
</tr>
</tbody>
</table>
Safety over EtherCAT

In the interest of realising safe data communication for EtherCAT, the Safety over EtherCAT protocol has been disclosed. The protocol meets the requirements of IEC 61508 up to Safety Integrity Level (SIL) 3 and of IEC 61784-3, as approved by the TÜV.

EtherCAT is used as a single-channel communication system. The transport medium is regarded as a "black channel" and not included in the safety considerations. Thus, the protocol can also be transmitted by other communication systems, backplanes, WLAN. The cyclic exchange of safe data between a Safety over EtherCAT master and a Safety over EtherCAT slave is referred to as a connection that is monitored via a watchdog timer.

The license for implementation of the Safety over EtherCAT master and slave technology in a device is free of charge.

ET9402 | Safety over EtherCAT Conformance Test Tool

The FSoE Conformance Test Tool (FSoE CTT) enables in-house testing of Safety over EtherCAT (FSoE) slave devices with EtherCAT interface. The utilisation of the ET9402 tool during the development of Safety over EtherCAT devices helps to ensure the conformity and to prepare the device for the official, independent FSoE Conformance Test in an ETG accredited EtherCAT Test Center.

The tool is based on the EtherCAT Conformance Test Tool (ET9400) with extensions regarding to Safety over EtherCAT functionality. A valid subscription of the ET9400 is a prerequisite for the FSoE CTT.

The test includes a complete test set for testing the conformance of FSoE slave devices. The test set is approved by TÜV. According to the Safety over EtherCAT Conformance Test Policy of the EtherCAT Technology Group (ETG), every manufacturer of EtherCAT devices with Safety over EtherCAT is obliged to prove the compatibility of the Safety over EtherCAT implementation by means of the current version of the FSoE tests and the required FSoE Conformance Test Tools.

<table>
<thead>
<tr>
<th>ET9402</th>
<th>Safety over EtherCAT Conformance Test Tool</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>includes a complete test set for testing the conformance of FSoE slave devices</td>
</tr>
<tr>
<td></td>
<td>The test set is approved by TÜV.</td>
</tr>
<tr>
<td></td>
<td>A valid subscription of the ET9400 is a prerequisite for the FSoE CTT.</td>
</tr>
</tbody>
</table>

www.beckhoff.com/ET9402
ET9000, ET9200, ET9300 | EtherCAT development software

**ET9000 | EtherCAT configurator**

Through clear definition of the interfaces in the EtherCAT specification an EtherCAT master can be developed without having to develop a configurator at the same time. The EtherCAT configurator is aimed at EtherCAT master developers who want to use it or integrate and distribute it with their software package.

The Windows software for configuring an EtherCAT network includes a configurator for:

- reading XML device descriptions (ESI)
- generating XML configuration descriptions (ENI)

**Properties**

- online features
- scanning of EtherCAT networks
- diagnostics
- free-run online mode for commissioning
- topology representation
- Automation software interface starts the configurator as COM server.
- COM interface
- XML interface for parameter exchange between client and server

| Safety configuration, EL69xx Safety PLC (Safety over EtherCAT) |
| including embedded graphical user interface |
| EXE file, executable under Windows XP, Vista and Windows 7 (32 bit) |

The EtherCAT configurator is not required if the TwinCAT software from Beckhoff is used.

<table>
<thead>
<tr>
<th>Ordering information</th>
</tr>
</thead>
<tbody>
<tr>
<td>ET9000</td>
</tr>
<tr>
<td>license for using the EtherCAT configurator</td>
</tr>
</tbody>
</table>

▶ www.beckhoff.com/ET9000

**ET9200 | EtherCAT Master Sample Code**

The EtherCAT Master Sample Code is a user mode Windows application that demonstrates implementation of the EtherCAT master. The TR8200 workshop for EtherCAT master developers is based on the ET9200.

**Features**

- boot-up and configuration
- sending and receiving of “raw” EtherCAT frames to/from a network adapter
- management of EtherCAT slave states
- reading of XML configuration descriptions
- sending of the initialisation commands that are defined for the different state changes to the slave device
- mailbox communication
- CoE (CAN Application protocol over EtherCAT)
- SoE (Servodrive Profile over EtherCAT)
- EoE (Ethernet over EtherCAT)
- FoE (File Access over EtherCAT)
- AoE (ADS over EtherCAT)
- integrated virtual switch functionality
- cyclical process data communication
- distributed clocks state machine

The software is sent as source code and can be adapted to the hardware environment (Ethernet controller) and integrated in a real-time environment.

<table>
<thead>
<tr>
<th>Ordering information</th>
</tr>
</thead>
<tbody>
<tr>
<td>ET9200</td>
</tr>
<tr>
<td>license for using the EtherCAT Master Sample Code</td>
</tr>
</tbody>
</table>

▶ www.beckhoff.com/ET9200
ET9300 | EtherCAT Slave Stack Code

The EtherCAT Slave Stack Code (SSC) is a code written in ANSI C. Its modular and simple structure enables fast entry into slave development.

A large number of EtherCAT slaves can be realised with the SSC, from the I/Os to the drives. The stack can be easily adapted to different platforms since it provides a defined hardware access layer and also supports different controller architectures.

The SSC, which has been available since 2004 and has been continuously maintained and enhanced in collaboration with the EtherCAT Technology Group, is considered to a certain extent to be the reference for an EtherCAT slave device implementation. Particular attention was paid to the conformity with the protocol specification.

The slave stack code tool provided offers the possibility to generate a slave stack code, device description files (ESI) and individual source code documentation to suit the developer’s own needs.

Functionality (excerpt)

- ESM (EtherCAT State Machine)
- mailbox protocols:
  - CoE (CAN application protocol over EtherCAT)
  - AoE (ADS over EtherCAT)
- EoE (Ethernet over EtherCAT)
- FeO (File Transfer over EtherCAT)
- preparation for SoE (Servo drive profile over EtherCAT)
- preparation for boot loader support
- various synchronisations (e.g. DC), including Sync Watchdog
- example implementation of the CiA402 drive profile according to ETG.6010 specification

Analog & digital I/O
CiA402 Sample application
AoE CoE SoE EoE FeE
Mailbox Process data State machine (DC) Sync
Process data interface: serial/parallel
Platform: little-endian/big-endian, 16, 32 bit
EtherCAT Slave Controller

ET9400 | EtherCAT Conformance Test Tool

The Conformance Test Tool (CTT) enables in-house testing of EtherCAT slave devices. Use of the ET9400 tool during the development of an EtherCAT device assists in ensuring conformity and in preparing the device for the official, independent conformance test in an accredited EtherCAT Test Center (ETC) of the EtherCAT Technology Group.

The ET9400 tool requires a standard PC with Windows OS; special hardware is not required. The EtherCAT frames for stimulation of the device under test are sent via the standard Ethernet port. The tool processes the standard test cases supplied (XML files) sequentially. In this way, extensions of the test cases are possible without modifying the EtherCAT conformance test tool itself. The official test cases can be supplemented by their own routines. Test results and remarks are shown in a logger window and can in turn be saved as XML files. The tool also tests the electronic description of the EtherCAT device (EtherCAT slave information, ESI) and includes an editor for this file format, in order to be able to process the ESI file temporarily for test purposes. EEPROM (Slave Information Interface, SII) data can also be read, edited and written. Besides the items described above, the following items are also tested, among others:

- consistency check of CoE object directory, SII and ESI
- plausibility of device description information from SII and ESI
- test of EtherCAT State Machine (ESM)
- mailbox communication using SoE and CoE
- object dictionary consistency check according to CiA 402 profile
- extendable by FSoE protocol tests (ET9402 needed)

Ordering information

<table>
<thead>
<tr>
<th>ET9300</th>
<th>license for using the EtherCAT Slave Stack Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>ET9400</td>
<td>1-year license for using the EtherCAT Conformance Test Tool</td>
</tr>
</tbody>
</table>

www.beckhoff.com/ET9300

www.beckhoff.com/ET9400

We reserve the right to make technical changes.